

**What is claimed is:**

1. A thin film transistor liquid crystal display comprising:

a first substrate including a color filter having red, green, and blue filters and a black matrix disposed between the red, green, and blue filters;

a second substrate having red, green, and blue pixel regions respectively having red, green, and blue pixel electrodes, TFTs for applying electric signals to the pixel electrodes, gate and data lines connected to the TFTs and formed between the pixel regions, wherein TFTs for driving the green pixel electrodes are formed in the blue pixel regions; and

a liquid crystal layer disposed between the first and second substrates.

2. The thin film transistor liquid crystal display according to claim 1, wherein the red, green, and blue filters respectively correspond to the red, green, and blue pixel regions.

3. The thin film transistor liquid crystal display according to claim 1, further comprising:

electrode lead lines extending from the green pixel electrodes to the TFTs formed in the blue pixel regions for driving the green pixel electrodes.

4. The thin film transistor liquid crystal display according to claim 1, wherein the red, green, and blue pixel regions have an identical capacity to each other.

5. The thin film transistor liquid crystal display according to claim 1, wherein a width of the gate lines adjacent to the blue pixel regions is greater than that of the gate lines adjacent to other pixel regions.

6. The thin film transistor liquid crystal display according to claim 1, wherein each of the TFTs is formed to define an L-shaped channel between a respective source electrode and a respective drain electrode.

7. The thin film transistor liquid crystal display according to claim 1, wherein the TFTs for driving the green pixel electrodes are disposed adjacent to intersecting points of the gate lines and the data lines.

8. A structure for a liquid crystal display, comprising:  
plural pixel regions; and

plural thin film transistors (TFTs) for driving the pixel regions,

wherein the pixel regions include blue, green and red pixel regions, and TFTs for driving green pixel electrodes are formed on a portion of the blue pixel regions.

9. The structure according to claim 8, further comprising:

electrode lead lines extending from the green pixel electrodes in the green pixel regions to the blue pixel regions.

10. The structure according to claim 8, wherein the pixel regions have an identical capacity to each other.

11. The structure according to claim 8, further comprising:

gate lines and data lines crossing each other, wherein a width of the gate lines adjacent to the blue pixel regions is greater than that of the gate lines adjacent to other pixel regions.

12. The structure according to claim 8, wherein each of the TFTs is formed to define an L-shaped channel.

13. A thin film transistor liquid crystal display comprising:

red, green and blue pixel regions;

TFTs (thin film transistors) for driving blue pixel electrodes and TFTs for driving green pixel electrodes, all of which are formed in the blue pixel regions;

a TFT array substrate including the TFTs for driving the green pixel electrode and an electrode lead line for connecting the green pixel electrodes to the TFTs for driving the green pixel electrodes;

a color filter substrate; and

a liquid crystal layer disposed between the TFT array substrate and the color filter substrate.

14. The thin film transistor liquid crystal display according to claim 13, wherein the TFTs for driving the green pixel electrodes are formed between the pixel electrodes.

15. The thin film transistor liquid crystal display according to claim 13, wherein each of the TFTs is formed to define an L-shaped channel so that the electrode lead line can be easily formed.

16. A display device structure comprising:

a first substrate including a first color filter, a second color filter and a third color filter; and

a second substrate including,

a first pixel region, a second pixel region and a third pixel region corresponding respectively to the first, second, and third color filters,

a first switching device for driving the first pixel region, and

a second switching device for driving the second pixel region,

wherein the first and second switching devices are provided in the first pixel region.

17. The display device structure according to claim 16, wherein the first pixel region is a pixel region corresponding to the first color filter being a blue color filter.

18. The display device structure according to claim 17, wherein the first and second switching devices are thin film transistors (TFTs) for driving pixel electrodes in the first and second pixel regions being blue and green pixel regions, respectively.

19. The display device structure according to claim 16, further comprising:

a lead line extending from a pixel electrode in the second pixel region to the second switching device in the first pixel region.

20. The display device structure according to claim 16, wherein the first and second switching devices are thin film transistors (TFTs), each of the TFTs including a gate electrode, a source electrode, a drain electrode, and an L-shaped channel formed by and between the source and drain electrodes.

21. The display device structure according claim 16, wherein the first switching device is symmetrically located with respect to the second switching device.